

**GO TRANSIT SIGNALS & COMMUNICATIONS
CODES OF PRACTICE**

VRLA/NICAD Battery C/8 Deep-Cycle Discharge Record

SUBDIVISION	DATE	LOCATION OF BATTERY (MILE)
-------------	------	----------------------------

BANK DESIGNATION _____ DESIGN CAPACITY _____ (A-HR) DESIGN LIFE _____ (YRS) END VOLTAGE (#CELLS X 1.65/1.00) _____ V
 MODEL _____ # OF CELLS _____ INSTALLED DATE _____ CURRENT: NORMAL _____ (A) LOAD _____ (A)

TEST TIME (HH:MM)	ACTUAL TIME (HH:MM)	BATTERY VOLTAGE (V) TEMP (F)	INDIVIDUAL CELL VOLTAGE												BATTERY CURRENT (A)	INTERVAL A-HR (AMPS/4)
			1	2	3	4	5	6	7	8	9	10	11	12		
0:00																
0:15																
0:30																
0:45																
1:00																
1:15																
1:30																
1:45																
2:00																
2:15																
2:30																
2:45																
3:00																
3:15																
3:30																
3:45																
4:00																
4:15																
4:30																
4:45																
5:00																
5:15																
5:30																
5:45																
6:00																
6:15																
6:30																
6:45																
7:00																
7:15																
7:30																
7:45																
8:00																

ACTUAL CAPACITY (SUM COLUMN) -->

%CAPACITY (ACTUAL CAPACITY/DESIGN CAPACITY x 100) -->

%